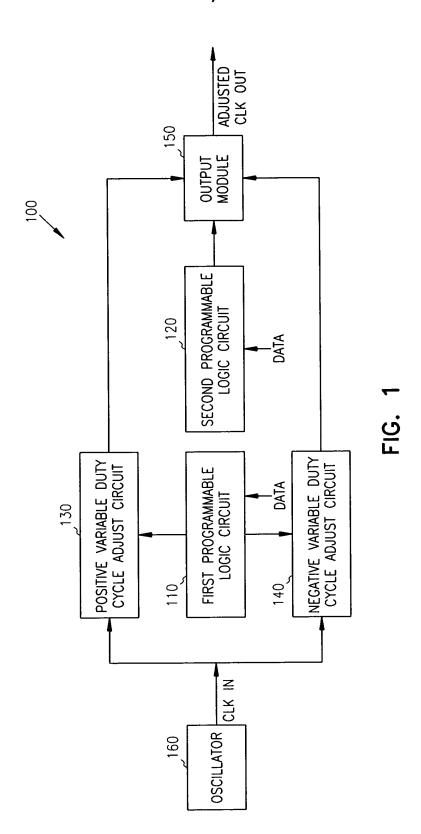
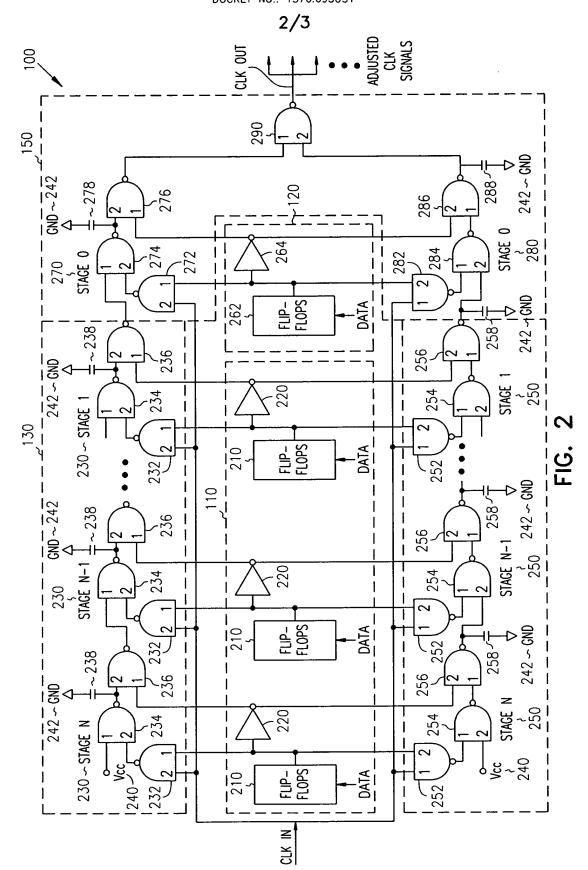
1/3



TITLE: CLOCK SIGNAL DUTY CYCLE ADJUST CIRCUIT INVENTORS NAME: Mark S. Birrittella et al. DOCKET NO.: 1376.693US1

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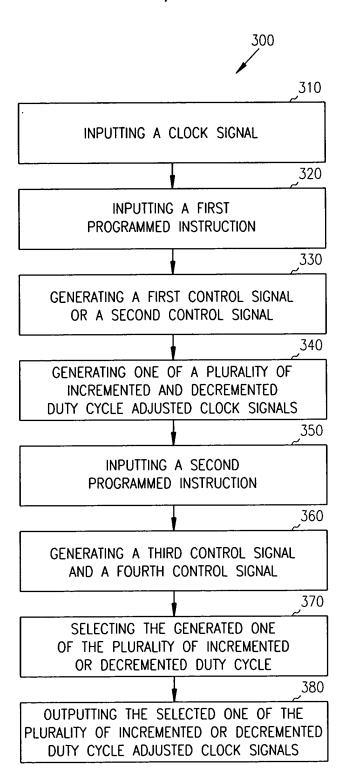


FIG. 3